

# AMENDMENT TRANSMITTAL LETTER

Docket No.  
M4065.0340/P340

Application No.  
09/652,003-Conf. #2935

Filing Date  
August 31, 2000

Examiner  
W. H. Choi

Art Unit  
2186

Applicant(s): Graham Kirsch

Invention: METHOD AND APPARATUS FOR CONNECTING A MASSIVELY PARALLEL  
PROCESSOR ARRAY TO A MEMORY ARRAY IN A BIT SERIAL MANNER

## TO THE COMMISSIONER FOR PATENTS

RECEIVED

Transmitted herewith is an amendment in the above-identified application.

APR 19 2004

The fee has been calculated and is transmitted as shown below.

Technology Center 2100

### CLAIMS AS AMENDED

	Claims Remaining After Amendment	Highest Number Previously Paid	Number Extra Claims Present	Rate	
Total Claims	45	- 48 =		x	0.00
Independent Claims	15	- 15 =		x	0.00
Multiple Dependent Claims (check if applicable) <input type="checkbox"/>					
Other fee (please specify):					
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT:					0.00

☒ Large Entity

☐ Small Entity

☒ No additional fee is required for this amendment.

☐ Please charge Deposit Account No. \_\_\_\_\_ in the amount of \$ \_\_\_\_\_.  
A duplicate copy of this sheet is enclosed.

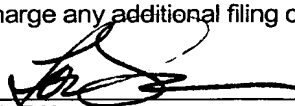
☐ A check in the amount of \$ \_\_\_\_\_ to cover the filing fee is enclosed.

☐ Payment by credit card. Form PTO-2038 is attached.

☒ The Director is hereby authorized to charge and credit Deposit Account No. 04-1073  
as described below. A duplicate copy of this sheet is enclosed.

☒ Credit any overpayment.

☒ Charge any additional filing or application processing fees required under 37 CFR 1.16 and 1.17.

  
Thomas J. D'Amico  
Attorney Reg. No.: 28,371

Dated: April 13, 2004

DICKSTEIN SHAPIRO MORIN & OSHINSKY LLP  
2101 L Street NW  
Washington, DC 20037-1526  
(202) 828-2232



18/D  
C.B.  
4/27/04

Docket No.: M4065.0340/P340  
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:  
Graham Kirsch

Confirmation No.: 2935

Application No.: 09/652,003

Art Unit: 2186

Filed: August 31, 2000

Examiner: W. H. Choi

For: METHOD AND APPARATUS FOR  
CONNECTING A MASSIVELY PARALLEL  
PROCESSOR ARRAY TO A MEMORY  
ARRAY IN A BIT SERIAL MANNER

RECEIVED

APR 19 2004

Technology Center 2100

AMENDMENT IN RESPONSE TO NON-FINAL OFFICE ACTION

MS Non-Fee Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

INTRODUCTORY COMMENTS

In response to the Office Action dated January 13, 2004 (Paper No. 17), please amend the above-identified U.S. patent application as follows:

**Amendments to the Claims** are reflected in the listing of claims which begins on page 2 of this paper.

**Remarks/Arguments** begin on page 28 of this paper.